

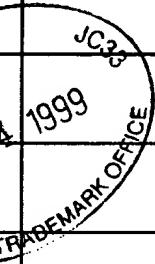
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<p>Sheet</p>		<p>1</p>	<p>of</p>	<p>1</p>	<p>Attorney Docket Number</p>
<p>Application Number</p>		<p>09/301,853</p>			
<p>Filing Date</p>		<p>April 29, 1999</p>			
<p>First Named Inventor</p>		<p>Kazuhisa OHBUCHI</p>			
<p>Group Art Unit</p>		<p>2745-2133</p>			
<p>Examiner Name</p>		<p>G. LAMARRE</p>			

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		K' K YAMAGUCHI et al. "TURBO CODE", A NEW CODING SYSTEM APPROACHING THEORETICAL SHANNON LIMITS, IS BORN IN FRANCE ARTICLE IN "NIKKEI ELECTRONICS" NO. 721, Published 7/13/98; page 168	
		column 3 line 27 to page 169 column 3 line 5 is relevant to the present invention.	
		RECEIVED JUN 21 1999 Group 2700	

Examiner Signature	Guy Lamare	Date Considered	8/16/01
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